



PATENT

#9/Response
5/21/03
John H

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: SCHAUER ET AL 1 PCT
SERIAL NO.: 09/890,656 EXAMINER: T.F. TRAN
FILED: AUGUST 2, 2001 GROUP: 2811
TITLE: SEMICONDUCTOR WAFER COMPRISING A THIN EPITAXIAL
SILICON LAYER AND METHOD FOR PRODUCING THE SAME

RESPONSE TO A FIRST OFFICE ACTION

ATTN: MAIL STOP FEE AMENDMENT
Assistant Commissioner for Patents
P.O. BOX 1450
Alexandria, VA 22313-1450

Dear Sir:

This is in response to the First Office Action dated
January 13, 2003, with the time for reply having been
extended for one month, from APRIL 13, 2003, until MAY 13,
2003.

The Remarks begin on Page 2.

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MAY 20 2003
TECHNOLOGY CENTER 2800